INDIAN INSTITUTE OF TECHNOLOGY

KHARAGPUR – 721 302, INDIA

Sponsored Research and Industrial Consultancy

ADVERTISEMENT NO. : IIT/SRIC/R/RVI/2026/111, DATED 2026-11-27

Applications are invited through online for the following assignment in a purely time bound research project undertaken in the Department of Computer Science and Engineering of this institute.

Name of the temporary assignment	:	1) Junior Research Fellow (JRF) - Research, No. of post: 1.
Name of the temporary research project	:	Automation and Setting Up of a Reference Lab for Validating Cryptographic Algorithms/Modules as per ISO/IEC Standards (Project Code – R/RVI)
Dept./Centre/School	:	1) Computer Science and Engineering
PI/Co-PI	:	Prof./Dr. Aritra Hazra
Application fee(In Rs.)	:	General / Male / OBC: 0 , Female : 0, SC/ST: 0
Last date of application	:	December 11, 2026
Name of the Sponsoring Agency	:	Ministry of Electronics and Information Technology(Government of IndiaElectronics Niketan, 6, CGO Complex,Lodhi Road, New Delhi - 110003)
Consolidated Compensation (per month)	:	1) upto 37000 (depending on qualification and experience)
Qualifications & Experience	:	1) Essential Qualification : Four years Bachelor?s Degree in Biology related Sciences / Master?s Degree in Biological sciences / /B. Pharm or Bachelor?s degree in Engineering or Technology / M. Pharm or Masters? degree in Engineering or Technology or in medicine in from a recognized University or equivalent. Selected through a process described through any one of the following: a. Scholars who are selected through National Eligibility Tests - CSIR-UGC NET including lectureship (Assistant Professorship) and GATE. b. The selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER etc. Desired Experience : The applicant MUST have at least 8.5 CGPA or 80% marks throughout his all examinations starting from Class 10. Desired Experience : - Knowledge in Cryptography and Hardware Security; - Proficiency in Programming (preferably in C/C++); - Knowledge of Hardware Design Language (like Verilog or SystemVerilog). Age Limit : 35 years. Age rules may be relaxed upon fulfilling sponsor agency rules if any. Tenure : 13 Months or the termination of the project, whichever is earlier.

Note:1. The qualifications and experience prescribed are the minimum and mere possession of the same does not entitle a candidate to be called for the interview.
2. Age as on last date of advertisement.

Application fee shall be deposited in the following bank account through RTGS/NEFT/NET Banking etc. as under:
1).BANK NAME: STATE BANK OF INDIA(00202);2).BANK ADDRESS: P.O:KHARAGPUR TECHNOLOGY,DIST:MIDNAPORE;WEST BENGAL-721302;3).NAME OF BANK A/C: IIT SRIC-OM Account;4).ACCOUNT NO: 39502115210;5).IFSC CODE: SBIN0000202;6).SWIFT CODE: SBININBB339;
The transaction details(UTR No./Transaction ID etc.) should be mentioned in the Application Form.