



INDIAN INSTITUTE OF TECHNOLOGY
KHARAGPUR – 721 302, INDIA
Sponsored Research and Industrial Consultancy

ADVERTISEMENT NO. : IIT/SRIC/R/NGS/2026/109, DATED 2026-07-28

Applications are invited through online for the following assignment in a purely time bound research project undertaken in the Department of Computer Science and Engineering of this institute.

- Name of the temporary assignment : 1) Senior Research Fellow (SRF) - Research, No. of post: 1.
- Name of the temporary research project : Secure and Intelligent Indigenous Network Packet Processor on Field Programmable Gate Array with Hardware based Stateful Firewall for Real-time Intrusion Detection/Prevention (Project Code – R/NGS)
- Dept./Centre/School : 1) Computer Science and Engineering
- PI/Co-PI : Prof./Dr. Rajat Subhra Chakraborty
- Application fee(In Rs.) : General / Male / OBC: 0
, Female : 0, SC/ST: 0
- Last date of application : August 31, 2026
- Name of the Sponsoring Agency : Directorate of Futuristic Technology Management (DFTM), Defence Research and Development Organisation, Ministry of Defence(400A, DRDO Bhawan Rajaji Marg, New Delhi - 110011)
- Consolidated Compensation (per month) : 1) upto 42000
(depending on qualification and experience)
- Qualifications & Experience : 1)
Essential Qualification :
Post Graduate Degree in Basic Science OR Graduate / Post Graduate Degree in Professional Course selected through a process described through any one of the following:
a. Scholars who are selected through National Eligibility Tests - CSIR-UGC NET including lectureship (Assistant Professorship) and GATE.
b. The selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER etc.
With two years of research experience
- Desired Experience :
1. M.Tech./M.E. in CS/CSE/IT/ECE/EE or related discipline.
2. VLSI design on FPGA/ASIC platform using Verilog/VHDL.
- Age Limit : 32 years. Age rules may be relaxed upon fulfilling sponsor agency rules if any.
- Tenure : 28 Months or the termination of the project, whichever is earlier.

Note:1. The qualifications and experience prescribed are the minimum and mere possession of the same does not entitle a candidate to be called for the interview.
2. Age as on last date of advertisement.

Application fee shall be deposited in the following bank account through RTGS/NEFT/NET Banking etc. as under:
1).BANK NAME: STATE BANK OF INDIA(00202);2).BANK ADDRESS: P.O:KHARAGPUR TECHNOLOGY,DIST:MIDNAPORE;WEST BENGAL-721302;3).NAME OF BANK A/C: IIT SRIC-OM Account;4).ACCOUNT NO: 39502115210;5).IFSC CODE: SBIN0000202;6).SWIFT CODE: SBININBB339;
The transaction details(UTR No./Transaction ID etc.) should be mentioned in the Application Form.