



**INDIAN INSTITUTE OF TECHNOLOGY**  
**KHARAGPUR – 721 302, INDIA**  
*Sponsored Research and Industrial Consultancy*

**Mr. Rajarshi Banerjee**

*Administrative Officer (Projects)*

**ADVERTISEMENT NO. : IIT/SRIC/R/NGS/2026/109, DATED August 25,2026**

Applications are invited on plain paper for the following assignment in a purely time bound research project undertaken in the Department of Computer Science and Engineering of this institute for a period of one year from the date of joining with a provision of extension for a maximum period up to four years or termination of the project, whichever is earlier, based on performance.

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|-------------------------------------------|---|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 1. Name of the temporary assignment       | : | 1) Senior Research Fellow (SRF) 1 post/s                                                                                                                                                              |
| 2. Name of the temporary research project | : | Secure and Intelligent Indigenous Network Packet Processor on Field Programmable Gate Array with Hardware based Stateful Firewall for Real-time Intrusion Detection/Prevention (Project Code – R/NGS) |
| 3. Name of the Sponsoring Agency          | : | Directorate of Futuristic Technology Management (DFTM), Defence Research and Development Organisation, Ministry of Defence                                                                            |
| 4. Consolidated Compensation (per month)  | : | 1) Rs. 42000 /-<br>(depending on qualification and experience)                                                                                                                                        |

5. Qualifications & Experience :

1) Rs. Post Graduate Degree in Basic Science OR Graduate / Post Graduate Degree in Professional Course selected through a process described through any one of the following:

a. Scholars who are selected through National Eligibility Tests - CSIR-UGC NET including lectureship (Assistant Professorship) and GATE.

b. The selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER etc. With two years of research experience

1. M.Tech./M.E. in CS/CSE/IT/ECE/EE or related discipline.

2. VLSI design on FPGA/ASIC platform using Verilog/VHDL. /-

The qualifications and experience prescribed are the minimum and mere possession of the same does not entitle a candidate to be called for the interview.

(Rajarshi Banerjee)