



INDIAN INSTITUTE OF TECHNOLOGY
KHARAGPUR – 721 302, INDIA
Sponsored Research and Industrial Consultancy

Mr. Rajarshi Banerjee

Administrative Officer (Projects)

ADVERTISEMENT NO. : IIT/SRIC/R/NBY/2026/102, DATED August 27,2026

Applications are invited on plain paper for the following assignment in a purely time bound research project undertaken in the Department of G.S Sanyal School of Telecommunication of this institute for a period of one year from the date of joining with a provision of extension for a maximum period up to four years or termination of the project, whichever is earlier, based on performance.

- | | | |
|---|---|--|
| 1. Name of the temporary assignment | : | 1) Junior Research Fellow (JRF) 1 post/s |
| 2. Name of the temporary research project | : | Efficient Hardware Architecture of Non-Binary LDPC and Turbo Channel Coding for Satellite Communication (Project Code – R/NBY) |
| 3. Name of the Sponsoring Agency | : | KCSTC |
| 4. Consolidated Compensation (per month) | : | 1) Rs. 37000 /-
(depending on qualification and experience) |

5. Qualifications & Experience :

1) Rs. Post Graduate Degree in Basic Science OR Graduate / Post Graduate Degree in Professional Course selected through a process described through any one of the following: a. Scholars who are selected through National Eligibility Tests - CSIR-UGC NET including lectureship (Assistant Professorship) and GATE. b. The selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER etc.

B.E/B.Tech in ECE/EE with strong foundation in digital logic and digital communication /-

The qualifications and experience prescribed are the minimum and mere possession of the same does not entitle a candidate to be called for the interview.

(Rajarshi Banerjee)