



भारतीय प्रौद्योगिकी संस्थान खड़गपुर

खड़गपुर - 721 302, भारत

Indian Institute of Technology Kharagpur

Kharagpur - 721302, India

सह-संकायाध्यक्ष

Associate Dean

सीई एंड टी / CE & T

Date: 13-05-2025

To,  
Girish N Bhaviyavar  
Senior Faculty/DGM  
HAL Management Academy (HMA)  
Bengaluru, 560037

Dear Sir/Madam,

Sub: Revised Quotation for course on Thermal Design & Management of Electronic Equipment for Defence Applications

In reference to email dated 06/05/2025 sent to Associate Dean (CE&T), IIT Kharagpur and your recent correspondence with Prof. Anandaroop Bhattacharya at IIT Kharagpur, we hereby enclose the course details and revised price quotation for the course on "Thermal Design and Management of Electronic Equipment for Defence Applications" at HAL Management Academy (HMA). The course will be taught by Prof. Anandaroop Bhattacharya of Mechanical Engineering Department.

With regards,

**Authorized Signature with Name**

**& Seal of the Agency/Institution**

Prof. Haimanti Banerji

Associate Dean

(CE & T)

IIT Kharagpur 721302

Sub: "Thermal Design & Management of Electronic Equipment for Defence Applications" course at HAL Management Academy (HMA)

Duration: 2 days (approx. 12 hours including test and evaluation)

Sl. No.	Item
Day 1	<p><b>Fundamentals of Electronic Packages</b></p> <ol style="list-style-type: none"> <li>Electronic packaging – definitions and terminology</li> <li>First Level Packaging <ol style="list-style-type: none"> <li>Chip and chip carrier, lead-frame packages</li> <li>Plastic and ceramic packaging</li> <li>Peripheral and Area array packages</li> <li>Interconnection types and methods (wire bonds, solder joints, TSVs)</li> <li>Flip Chip packaging</li> </ol> </li> <li>Advanced Packaging and Heterogeneous Integration <ol style="list-style-type: none"> <li>Multi-chip Modules</li> <li>3D Stacked Dies Packages</li> <li>System in package (SIP) vs. System on chip (SOC)</li> <li>Heterogeneous Integration and Chiplets</li> </ol> </li> </ol> <p><b>Thermal Technologies and Design</b></p> <ol style="list-style-type: none"> <li>Heat Transfer fundamentals</li> <li>Thermal Resistance</li> <li>Cooling technologies – air cooling, liquid cooling, refrigeration, two-phase flows</li> </ol> <p><b>Heat Sink Design and calculations – focus on avionics systems</b></p> <p><b>Heat pipes and vapour chambers – theory and calculations</b></p>
Day 2	<p><b>Thermal Interface Materials</b></p> <ol style="list-style-type: none"> <li>Interface resistance</li> <li>Types of TIMs</li> <li>Measurement and characterisation</li> <li>Reliability assessment</li> </ol> <p><b>Package and board level heat flow paths</b></p> <ol style="list-style-type: none"> <li>Understanding heat transfer paths</li> <li>Resistance network</li> <li>Heat dissipation estimation for at component, board and system level.</li> </ol> <p><b>Transient Heat transfer</b></p> <ol style="list-style-type: none"> <li>Transient heat transfer</li> <li>Resistance – Capacitance network</li> <li>Heat dissipation estimation for at component, board and system level.</li> </ol> <p><b>Special Topics</b></p> <ol style="list-style-type: none"> <li>Heat Transfer enhancement techniques at sub-zero and high operational temperatures</li> <li>MIL grade fans</li> <li>Recent advances in cooling techniques</li> </ol> <p><b>Test and evaluation</b></p>

Sl. No.	Item	Amount in INR (Excluding taxes, GST and duties)
1.	<b>Total Price for Program</b>  inclusive of professional fee (honorarium), course material charges, Institute administrative charges and other miscellaneous charges	150,000/-  (Rupees One Lakh and Fifty Thousand only)

*Note: Total Price mentioned above is exclusive of Travelling, Boarding, Lodging, Taxes (including GST) and Duties*

PAN Number: AAAJI0323G

GST Number: 19AAAJI0323G1ZM

Applicable GST: 18%

 13/05/2025  
**Authorized Signature with Name**

**& Seal of the Agency/Institution**  
Prof. Haimanti Banerji  
Associate Dean  
(CE & T)  
IIT Kharagpur 721302

Date: 13/05/2025

Place: Kharagpur, West Bengal