







# SPARC-UKIERI COURSE ON SEMICONDUCTOR SUPPLY CHAIN NETWORK DESIGN 07-14 AUGUST 2025

#### Overview

The semiconductor industry is highly capital-intensive due to the complex nature of its manufacturing processes and supply chain. Nowadays, semiconductor Industries are facing a competitive business environment due to globalization, shorter product life cycles, minimum cost and lead time, new technologies, and disruption. This course will focus specifically on key steps to achieve efficient management and analysis of Semiconductor Supply Chain Network Design. Each lecture will be devoted to a specific section containing theoretical and practical aspects, related tools and techniques, case studies, group work, plenary discussion, and problemsolving activities. During this course, the participants would get a chance to leverage the knowledge of eminent faculties from around the globe in the field of supply chain management which will help them gain a practical insight into the problems which are plaguing various semiconductor industries.

### **Program Details**

Modules	15 hours lecture and 5 hours lab
Target	UG 4th Year, PG, PhD, students
Participants	interested in supply chain and industrial
_	engineering
Course Fees	There are no fees. Due to limited seats,
	the selection is on a first-come, first-
	served basis.
Accomodation	Twin-sharing accommodation will be
	provided on request
Venue	Department of ISE, IIT Kharagpur
Registration	https://erp.iitkgp.ac.in/CEP/courses.htm
Contact	Dr. Sri Krishna Kumar (convenor)
	Dr. Akhilesh Kumar (Co-Convenor)
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#### **Course Contents**

Topic	Contents	# of
_		hours
Introduction	Fundamentals of	1.5
	Semiconductor Supply	
	Chain, Semiconductor	
	Manufacturing Process	
Demand	The Demand Planning	1.5
Planning	activities related to the	
J	Sales function on the mid	
	and short-term levels	
Strategic	Integration, Outsourcing,	3
Network Design	coordination, Facility	
	Location	
Supply Chain	Make to Order, Make to	3
Strategies	Stock, Procurement,	
	Production	
Capacity	Master Planning, Resource	1.5
Planning	allocation	
Inventory	Safety Stock, Stochastic	1.5
Management	inventory models	
Production	Setup time, lead time,	1.5
Planning and	Scheduling, WIP	
Scheduling		
Lot Sizing	Cycle time, yield	1.5

Lab component

Experiment	<b>Brief Description</b>	# of hours
Optimization in Semiconductor Manufacturing	Linear and Integer programming problems in semiconductor manufacturing	2.5
Semiconductor Supply Chain Simulation	Discrete Event Simulation in semiconductor manufacturing	2.5

## The Faculty



Prof. Alok Choudhary is the Head of the Supply Chain Group at WMG, University of Warwick, and Founding Director of the JLR-WMG Supply Chain Innovation Hub. A sought-after supply chain thought leader and keynote speaker, his industry-driven research on sustainability,

resilience, and digital transformation drives impact across industry, policymakers, and society. Alok has led large-scale, multi-stakeholder projects with funding exceeding £25 million by UKRI, EU and industries including JLR, Siemens and DHL.



Dr. Sri Krishna Kumar is an Associate Professor in the in the Department of Industrial & Systems Engineering at the Indian Institute of Technology, Kharagpur. His expertise includes, Supply Chain Management, Port Logistics, Knowledge Management, Game Theory

and Optimization and published good articles in the journal like IJPR, CAIE, ANOR, and JEM.



Dr. Akhilesh Kumar is currently working as an Associate Professor in the Department of Industrial & Systems Engineering at the Indian Institute of Technology, Kharagpur. His publications appeared in such journals as the International Journal of Production

Economics, European Journal of Operational Research, Expert System with Applications, and IEEE. He has carried out various research and consultancy projects with Shell, Tata Sons, Govt. of India, European Union. Dr. Kumar is the recipient of the Faculty Excellence Award 2020.